

## **Amendments to the Specification**

Please amend the specification under 37 CFR §1.121(b)(1)(i) and (ii) by adding the heading CROSS-REFERENCE TO RELATED U.S. APPLICATION and its paragraph at page 1, at the top of the page beneath the title and before the first line as follows:

### **CROSS-REFERENCE TO RELATED U.S. APPLICATION**

This is a divisional of U.S. Application Serial No. 10/059,505, filed January 29, 2002.

Please replace the paragraph at page 27, line 29 through page 28, line 14 with the following paragraph:

In the operation of block 222, the AECD operational state values provided to inputs ST1-STN are compared by the inequality logic blocks 232<sub>1</sub> – 232<sub>N</sub> to the zero values of blocks 234<sub>1</sub> – 234<sub>N</sub>. For each of the AECD operational state values that are active; e.g., logic high level, the inequality is true and the corresponding B0 – BN-1 input of the N-bit packer block 236 is accordingly a logic high level. Conversely, for each of the AECD operational state values that are inactive, e.g., some level other than a logic high level, the inequality is false and the corresponding B0 – BN-1 input of the N-bit packer block 236 is accordingly a logic low level. Packer block 236 is operable to pack the various input values provided to it into an N-bit data structure representative of the operational state of each of the AECDs included within the emission manager block 56. The combustion manager block 58 is preferably configured to unpack this data structure

for processing thereof in a manner that is more fully described in [[co-pending]] U.S. [[Application Ser.]] Patent No. [[                ]] 6,705,301, entitled SYSTEM FOR PRODUCING CHARGE FLOW AND EGR FRACTION COMMANDS BASED ON ENGINE OPERATING CONDITIONS, which is assigned to the assignee of the present invention, and the contents of which have been incorporated herein by reference.